

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**(Under 37 CFR 1.97(b) or 1.97(d))**Docket No.
CH920020048US1In Re Application Of: **Georgious Cheliotis, et al.**Serial No.
10/625,177Filing Date
July 23, 2003Examiner
UnknownGroup Art Unit
UnknownTitle: **METHOD AND SYSTEM FOR PROVIDING A COMPUTING RESOURCE SERVICE**

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

☐ the fee set forth in 37 CFR 1.17(p).

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

(Under 37 CFR 1.97(b) or 1.97(c) P E)

Docket No.
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OCT 24 2003

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METHOD AND SYSTEM FOR PROVIDING A COMPUTING RESOURCE SERVICE

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 50-0510/IBM as described below.
- ☐ Charge the amount of _____
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- ☒ Charge any additional fee required.

Certificate of Transmission by Facsimile*

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I certify that this document and fee is being deposited on October 22, 2003 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Steven Fischman

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Signature

Dated: October 22, 2003

Steven Fischman

Registration No. 34,594

Correspondence Address

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Georgious Cheliotis, et al.

Examiner: Unknown

Serial No.: 10/625,177

Art Unit: Unknown

Filed: July 23, 2003

Docket No.: CH920020048US1 (16840)

For: METHOD AND SYSTEM FOR
PROVIDING A COMPUTING
RESOURCE SERVICE

Dated: October 22, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:


In accordance with 37 C.F.R. §§1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. A. Rosenberg, Optimal schedules for cycle-stealing in a network of workstations with a bag-of-tasks workload. *IEEE Transactions on Parallel and Distributed Systems*, 13(2):179-191, February 2002;
2. E. Heymann, M. Senar, E. Luque, and M. Livny. Evaluation of strategies to reduce the impact of machine reclaim in cycle-stealing environments. *IEEE 1st International Symposium on Cluster Computing and the Grid*, May 2001. pp. 320-328;

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on October 22, 2003.

Dated: October 22, 2003



Steven Fischman

3. K. Ryu and J. Hollingsworth. Exploiting fine grained idle periods in networks of workstations. *IEEE Transactions on Parallel and Distributed Systems*, 11(7):683–699, 2000;
4. A. Rosenberg. Guidelines for data-parallel cycle-stealing in networks of workstations, ii: On maximizing guaranteed output. IEEE 10th Symposium on Parallel and Distributed Processing, April 1999. pp 520–524; and
5. S. Leutenegger and X.-H. Sun. Limitations of cycle stealing for parallel processing on a network of homogeneous workstations. *Journal of Parallel and Distributed Computing*, 43:169–178, 1997.

Applicants are submitting copies of the above-cited references.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. §1.97(b), no petition, certification or fee is required.

Respectfully submitted,


Steven Fischman
Registration No. 34,594

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(Use several sheets if necessary)

CH920020048US1 (16840)

10/625.177

Georgios Cheliotis, et al.

July 23, 2003

Unknown

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/NV/		A. Rosenberg, Optimal schedules for cycle-stealing in a network of workstations with a bag-of-tasks workload. IEEE Transactions on Parallel and Distributed Systems, 13(2):179-191, February 2002
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EXAMINER /Naresh Vignani/

DATE CONSIDERED 04/27/2008

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

CH920020048US1 (16840)

Application Number

10/625,177

Applicant(s)

Georgious Cheliotis, et al.

Filing Date

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Group Art Unit

Unknown

*EXAMINER
INITIAL

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(Include Author, Title, Date, Pertinent Pages, Etc.)

/NV/

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